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7 Series FPGA Overview - 4 © Copyright 2011 Xilinx . Architecture Alignment Common elements enable easy IP reuse for quick design portability across all 7 series families - Design scalability from low -cost to high-performance - Expanded eco-system support - Quickest TTM . Precise, Low Jitter Clocking . MMCMs . Logic Fabric . LUT-6 CLB

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1. Each 7 series FPGA slice contains four LUTs and eight flip-flops; only some slices can use their LUTs as distributed RAM or SRLs. 2. Each DSP slice contains a pre-adder, a 25 x 18 multiplier, an adder, and an accumulator. 3. Block RAMs are fundamentally 36 Kb in size; each block can also be used as two independent 18 Kb blocks. 4.

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7 Series FPGA Overview - UCY Summary of 7 Series FPGA Features † Advanced high-performance FPGA logic based on real 6-input look- up table (LUT) technology configurable as distributed memory. † 36 Kb dual-port block RAM with built-in FIFO logic for on-chip data buffering. † High-performance SelectIO™ technology with support for DDR3 interfaces up to 1,866 Mb/s.

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A high-level introduction to the 7 Series product family and all of its device features. For additional video and instructor-led trainings please visit: www....

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Xilinx® 7 series FPGAs comprise four FPGA families that address the complete range of system requirements, ranging from low cost, small form factor, cost-sensitive, high-volume applications to ultra high-end connectivity bandwidth, logic capacity, and signal processing capability for the most demanding.

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greater" under overview. Changed "7 Series Features" heading to 7 Series FPGAs Configuration Differences from Previous FPGA Generations. Under this heading changed "D00" in note to "D0", clarified the fourth bullet, added the sixth and seventh bullets, clarified the eighth bullet, and added the last paragraph. Replaced Table 1-1.

[7-Series-FPGA-Configuration-Xilinx](#)

For the 7 series, Xilinx introduced a full line of scalable FPGAs, which includes a new low-cost Artix-7 family, a midrange Kintex-7 family, and a high-end Virtex-7 family. The base FPGA building blocks of logic cells, DSP blocks, BlockRAM, and so on are all consistent across the 7 series, making it much simpler to migrate designs.

[Advantages-of-Xilinx-7-Series-FPGA-and-SoC-Devices-NI](#)

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For the design of the power distribution system consult 7 Series FPGAs PCB Design and Pin Planning Guide (UG483). 3. If V CCINT and V CCBRAM are operating at the same voltage, V CCINT and V CCBRAM should be connected to the same supply.

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Summary of 7 Series FPGA Features † Advanced high-performance FPGA logic based on real 6-input look- up table (LUT) technology configurable as distributed memory. † 36 Kb dual-port block RAM with built-in FIFO logic for on-chip data buffering. † High-performance SelectIO™ technology with support for DDR3 interfaces up to 1,866 Mb/s.

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